

**IN THE CLAIMS:**

This listing of claims will replace all prior versions, and listings, of claims in the application:

1-41.(Canceled)

~~1~~  
~~42.~~(Currently Amended) A display device comprising:

a semiconductor substrate;

an insulating layer formed on the semiconductor substrate;

a switching transistor and a current controlling transistor formed on the insulating layer, each comprising a source region, a drain region, a gate electrode and a gate insulating film;

a first interlayer insulating film over the switching transistor and the current controlling transistor;

a source wiring and a drain wiring which are connected with the switching transistor and a source wiring and a drain wiring which are connected with the current controlling transistor, and formed over the first interlayer insulating film;

[[an]] a second interlayer insulating film formed over the source wiring and the drain wiring of the switching transistor and the source wiring and the drain wiring of the current controlling transistor;

an electrode electrically connected with one of the source ~~region~~ wiring and the drain ~~region~~ wiring of the switching transistor, and formed over the second interlayer insulating film;

a dielectric layer formed on the electrode;

a power supply line electrically connected with one of the source ~~region~~ wiring and the drain

region wiring of the current controlling transistor, and formed on the dielectric layer;

a first electrode electrically connected with the other one of the source region wiring and the drain region wiring of the current controlling transistor;

an EL layer formed over the first electrode; and

a second electrode formed over the EL layer.

~~2~~  
43.(Previously Presented) A display device according to claim ~~42~~, wherein the display device is incorporated in at least one selected from the group consisting of a portable telephone, a video camera, a mobile computer, a goggle type display, a projector, an electronic book, a digital camera, and a DVD player.

~~3~~  
44.(Previously Presented) A display device according to claim ~~42~~, wherein the first electrode overlaps the power supply line.

~~4~~  
45.(Previously Presented) A display device according to claim ~~42~~, wherein the electrode comprises one selected from the group consisting of Al, Ta and Ti.

~~5~~  
46.(Previously Presented) A display device according to claim ~~42~~, wherein the dielectric layer comprises an oxidation film of the electrode.

~~7~~  
47.(Currently Amended) A display device comprising:  
a semiconductor substrate;

an insulating layer formed on the semiconductor substrate;

a p-channel type switching transistor and an n-channel type current controlling transistor formed on the insulating layer, each comprising a source region, a drain region, a gate electrode and a gate insulating film;

a first interlayer insulating film over the p-channel type switching transistor and the n-channel type current controlling transistor;

a source wiring and a drain wiring which are connected with the p-channel type switching transistor and a source wiring and a drain wiring which are connected with the n-channel type current controlling transistor, and formed over the first interlayer insulating film;

[[an]] a second interlayer insulating film formed over the source wiring and the drain wiring of the p-channel type switching transistor and the source wiring and the drain wiring of the n-channel type current controlling transistor;

an electrode electrically connected with one of the source ~~region~~ wiring and the drain ~~region~~ wiring of the p-channel type switching transistor, and formed over the second interlayer insulating film;

a dielectric layer formed on the electrode;

a power supply line electrically connected with one of the source ~~region~~ wiring and the drain ~~region~~ wiring of the n-channel type current controlling transistor, and formed on the dielectric layer;

a first electrode electrically connected with the other one of the source ~~region~~ wiring and the drain ~~region~~ wiring of the n-channel type current controlling transistor;

an EL layer formed over the first electrode; and

a second electrode formed over the EL layer.

~~8~~  
~~48.~~(Previously Presented) A display device according to claim ~~47~~<sup>7</sup>, wherein the first electrode overlaps the power supply line.

~~9~~  
~~49.~~(Previously Presented) A display device according to claim ~~47~~<sup>7</sup>, wherein the electrode comprises one selected from the group consisting of Al, Ta and Ti.

~~10~~  
~~50.~~(Previously Presented) A display device according to claim ~~47~~<sup>7</sup>, wherein the dielectric layer comprises an oxidation film of the electrode.

~~11~~  
~~51.~~(Previously Presented) A display device according to claim ~~47~~<sup>7</sup>, wherein the display device is incorporated in at least one selected from the group consisting of a portable telephone, a video camera, a mobile computer, a goggle type display, a projector, an electronic book, a digital camera, and a DVD player.

~~13~~  
~~52.~~(Currently Amended) A display device comprising:  
a semiconductor substrate;  
a switching transistor and a current controlling transistor formed on the semiconductor substrate, each comprising a source region, a drain region, a gate electrode and a gate insulating film;  
a first interlayer insulating film over the switching transistor and the current controlling transistor;  
a source wiring and a drain wiring which are connected with the switching transistor and a

source wiring and a drain wiring which are connected with the current controlling transistor, and formed over the first interlayer insulating film;

[[an]] a second interlayer insulating film formed over the source wiring and the drain wiring of the switching transistor and the source wiring and the drain wiring of the current controlling transistor;

an electrode electrically connected with one of the source ~~region~~ wiring and the drain ~~region~~ wiring of the switching transistor, and formed over the second interlayer insulating film;

a dielectric layer formed on the electrode;

a power supply line electrically connected with one of the source ~~region~~ wiring and the drain ~~region~~ wiring of the current controlling transistor, and formed on the dielectric layer;

a first electrode electrically connected with the other one of the source ~~region~~ wiring and the drain ~~region~~ wiring of the current controlling transistor;

an EL layer formed over the first electrode; and

a second electrode formed over the EL layer.

<sup>14</sup>  
~~52~~ (Previously Presented) A display device according to claim <sup>13</sup>~~52~~, wherein the first electrode overlaps the power supply line.

<sup>15</sup>  
~~54~~ (Previously Presented) A display device according to claim <sup>13</sup>~~52~~, wherein the electrode comprises one selected from the group consisting of Al, Ta and Ti.

<sup>16</sup>  
~~55~~.(Previously Presented) A display device according to claim <sup>13</sup>~~52~~, wherein the dielectric layer comprises an oxidation film of the electrode.

<sup>17</sup>  
~~56~~.(Previously Presented) A display device according to claim <sup>13</sup>~~52~~, wherein the display device is incorporated in at least one selected from the group consisting of a portable telephone, a video camera, a mobile computer, a goggle type display, a projector, an electronic book, a digital camera, and a DVD player.

<sup>19</sup>  
~~57~~.( Currently Amended) A display device comprising:

- a semiconductor substrate;
- a p-channel type switching transistor and an n-channel type current controlling transistor formed on the semiconductor substrate, each comprising a source region, a drain region, a gate electrode and a gate insulating film;
- a first interlayer insulating film over the p-channel type switching transistor and the n-channel type current controlling transistor;
- a source wiring and a drain wiring which are connected with the p-channel type switching transistor and a source wiring and a drain wiring which are connected with the n-channel type current controlling transistor, and formed over the first interlayer insulating film;
- [[an]] a second interlayer insulating film formed over the source wiring and the drain wiring of the p-channel type switching transistor and the source wiring and the drain wiring of the n-channel type current controlling transistor;
- an electrode electrically connected with one of the source region wiring and the drain region

wiring of the p-channel type switching transistor, and formed over the second interlayer insulating film;

a dielectric layer formed on the electrode;

a power supply line electrically connected with one of the source ~~region~~ wiring and the drain ~~region~~ wiring of the n-channel type current controlling transistor, and formed on the dielectric layer;

a first electrode electrically connected with the other one of the source ~~region~~ wiring and the drain ~~region~~ wiring of the n-channel type current controlling transistor;

an EL layer formed over the first electrode; and

a second electrode formed over the EL layer.

<sup>20</sup>  
~~58~~. (Previously Presented) A display device according to claim <sup>19</sup>~~57~~, wherein the first electrode overlaps the power supply line.

<sup>21</sup>  
~~59~~. (Previously Presented) A display device according to claim <sup>19</sup>~~57~~, wherein the electrode comprises one selected from the group consisting of Al, Ta and Ti.

<sup>22</sup>  
~~60~~. (Previously Presented) A display device according to claim <sup>19</sup>~~57~~, wherein the dielectric layer comprises an oxidation film of the electrode.

23/  
61.

(Previously Presented) A display device according to claim 57, wherein the display device is incorporated in at least one selected from the group consisting of a portable telephone, a video camera, a mobile computer, a goggle type display, a projector, an electronic book, a digital camera, and a DVD player.

19/  
57.

25/  
62.

(Currently Amended) A display device comprising:

a semiconductor substrate;

a switching transistor and a current controlling transistor formed on the semiconductor substrate, each comprising a source region, a drain region, a gate electrode and a gate insulating film;

a first interlayer insulating film over the switching transistor and the current controlling transistor;

a source wiring and a drain wiring which are connected with the switching transistor and a source wiring and a drain wiring which are connected with the current controlling transistor, and formed over the first interlayer insulating film;

[[an]] a second interlayer insulating film formed over the source wiring and the drain wiring of the switching transistor and the source wiring and the drain wiring of the current controlling transistor;

an electrode electrically connected with one of the source ~~region~~ wiring and the drain ~~region~~ wiring of the switching transistor, and formed over the second interlayer insulating film;

a dielectric layer formed on the electrode;

a power supply line electrically connected with one of the source ~~region~~ wiring and the drain ~~region~~ wiring of the current controlling transistor, and formed on the dielectric layer;

a storage capacitance comprising the electrode, the dielectric layer and the power supply line;  
a first electrode electrically connected with the other one of the source region wiring and the drain region wiring of the current controlling transistor;  
an EL layer formed over the first electrode; and  
a second electrode formed over the EL layer.

<sup>26</sup>  
~~63~~ (Previously Presented) A display device according to claim ~~62~~<sup>25</sup>, wherein the first electrode overlaps the power supply line.

<sup>27</sup>  
~~64~~ (Previously Presented) A display device according to claim ~~62~~<sup>25</sup>, wherein the electrode comprises one selected from the group consisting of Al, Ta and Ti.

<sup>28</sup>  
~~65~~ (Previously Presented) A display device according to claim ~~62~~<sup>25</sup>, wherein the dielectric layer comprises an oxidation film of the electrode.

<sup>29</sup>  
~~66~~ (Previously Presented) A display device according to claim ~~62~~<sup>25</sup>, wherein the display device is incorporated in at least one selected from the group consisting of a portable telephone, a video camera, a mobile computer, a goggle type display, a projector, an electronic book, a digital camera, and a DVD player.

<sup>31</sup>  
~~67~~ (Currently Amended) A display device comprising:  
a semiconductor substrate;

a p-channel type switching transistor and an n-channel type current controlling transistor formed on the semiconductor substrate, each comprising a source region, a drain region, a gate electrode and a gate insulating film;

a first interlayer insulating film over the p-channel type switching transistor and the n-channel type current controlling transistor;

a source wiring and a drain wiring which are connected with the p-channel type switching transistor and a source wiring and a drain wiring which are connected with the n-channel type current controlling transistor, and formed over the first interlayer insulating film;

[[an]] a second interlayer insulating film formed over the source wiring and the drain wiring of the p-channel type switching transistor and the source wiring and the drain wiring of the n-channel type current controlling transistor;

an electrode electrically connected with one of the source ~~region~~ wiring and the drain ~~region~~ wiring of the p-channel type switching transistor, and formed over the second interlayer insulating film;

a dielectric layer formed on the electrode;

a power supply line electrically connected with one of the source ~~region~~ wiring and the drain ~~region~~ wiring of the n-channel type current controlling transistor, and formed on the dielectric layer;

a storage capacitance comprising the electrode, the dielectric layer and the power supply line;

a first electrode electrically connected with the other one of the source ~~region~~ wiring and the drain ~~region~~ wiring of the n-channel type current controlling transistor;

an EL layer formed over the first electrode; and

a second electrode formed over the EL layer.

~~32/~~  
~~68.~~(Previously Presented) A display device according to claim ~~31/~~ 67, wherein the first electrode overlaps the power supply line.

~~33/~~  
~~69.~~(Previously Presented) A display device according to claim ~~31/~~ 67, wherein the electrode comprises one selected from the group consisting of Al, Ta and Ti.

~~34/~~  
~~70.~~(Previously Presented) A display device according to claim ~~31/~~ 67, wherein the dielectric layer comprises an oxidation film of the electrode.

~~35/~~  
~~71.~~(Previously Presented) A display device according to claim ~~31/~~ 67, wherein the display device is incorporated in at least one selected from the group consisting of a portable telephone, a video camera, a mobile computer, a goggle type display, a projector, an electronic book, a digital camera, and a DVD player.

~~6/~~  
~~72.~~(Previously Presented) A display device according to claim ~~1/~~ 42, wherein the EL layer is organic.

~~12/~~  
~~73.~~(Previously Presented) A display device according to claim ~~7/~~ 47, wherein the EL layer is organic.

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~~74.~~ (Previously Presented) A display device according to claim ~~52~~<sup>13</sup>, wherein the EL layer is organic.

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~~75.~~ (Previously Presented) A display device according to claim ~~57~~<sup>19</sup>, wherein the EL layer is organic.

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~~76.~~ (Previously Presented) A display device according to claim ~~62~~<sup>62</sup>, wherein the EL layer is organic.

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~~77.~~ (Previously Presented) A display device according to claim ~~67~~<sup>31</sup>, wherein the EL layer is organic.